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ABSTRACT

Electron-phonon interactions play a crucial role in nano-electronic device performance. As the accurate calculation of these interactions requires huge computational resources, reduction of this burden without losing accuracy poses an important challenge. Here, we investigate the electron-phonon interactions of nano-devices using two first-principles-based methods in numerically efficient manners. The first method is the Lowest Order Approximation (LOA) version of the computationally burdensome self-consistent Born approximation method. The LOA method incorporates the effect of each phonon mode on the electronic current perturbatively. In this work, we theoretically resolve the discrepancy between two conventional approaches of direct LOA calculation. To validate the correct approach, we compared its output with a completely different method (second method) named Special Thermal Displacement (STD) method. The STD method uses non-interacting transport calculation of the displaced atomic configuration of a device. We apply both methods to two thin-film nanodevices: 2D silicon junctionless FET and n-i-n FET. Both methods justify each other by providing similar results and exhibiting important quantum phenomena, such as phonon-assisted subthreshold swing degradation and tunneling.

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I. INTRODUCTION

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As the nano-electronic devices get shorter and narrower, electronic transport exhibits significant quantum mechanical effects, such as quantum confinement¹ and tunneling.^{2,3} Besides these effects, electron-phonon interactions remain significant even in nanoscale devices.⁴⁻⁶ Accurate calculation of these effects has become a major challenge from the viewpoint of device simulations. The most difficult part of the calculation comes from the huge computational burden posed by the interactions between the electrons and the phonons. While the semiclassical transport theory failed to include quantum effects, the effective mass approximation also does not always ensure a correct calculation of the quantum effects. Deformation potentials (DPs) based Boltzmann transport equation (BTE) is quite suitable to calculate electron–phonon interactions for bulk materials.^{7–12} However, it ignores phonon-assisted subthreshold swing degradation in nanoscale devices.¹³ Therefore, the electronic properties of the nanodevices must be treated by the atomistic¹⁴ quantum mechanical approach despite its computational burden.

Atomistic quantum mechanical calculations use nonequilibrium Green's function (NEGF) formalism along with either a semi-empirical tight-binding method or first-principles density functional theory (DFT). The advantage of the first-principles method is that it does not require to be fitted to a particular device. Luisier *et al.* have applied the self-consistent Born approximation (SCBA) algorithm using the NEGF formalism on a tight-binding basis to calculate electron-phonon scattering of Si nanowire transistors.^{15–18} Although it is computed with huge supercomputing resources, it failed to show phonon-assisted source-to-drain tunneling in the OFF-current state.

The electron-phonon interactions can be rigorously calculated by perturbation theory-based SCBA method using density functional theory (DFT). However, the computational burden limits its application to molecular-scale systems.^{19,20} However, some approximations to the algorithm can unlock the scope of calculating electron-phonon scattering beyond molecular systems with modest computational resources. In this paper, we will investigate the approximation method called lowest order approximation (LOA).²¹⁻²⁶ The main difference between SCBA and LOA methods is that the SCBA method includes an infinite number of scattering diagrams, whereas the LOA method considers only the conserving diagrams.²⁶ The LOA method reduces the computational burden as it requires a much lesser number of iterations than SCBA to satisfy the conservation law. Hence, first order LOA needs the lowest computational cost as it requires only one iteration. Reference 23 shows that, in the weak-scattering regime, such as thin-film nanodevices, first order LOA is enough to show agreement with SCBA results. The LOA method considers the effect of each phonon mode perturbatively.

The LOA method can be implemented either by a direct algorithm²⁶ or by a rescaling technique.^{25,26} The direct algorithm involves the Langreth theorem,^{24,27} whereas the rescaling technique involves the Keldysh equation.^{22,27-30} However, some articles implemented the LOA method with a direct algorithm using the Keldysh equation instead of the Langreth theorem.^{20,31–35} In this article, we discuss the two conflicting approaches of the direct algorithm and theoretically resolve the issue.

A conceptually simple and efficient alternative to the perturbation theory-based LOA method is the stochastic sampling of lattice vibrations using Monte Carlo $(MC)^{36,37}$ or Molecular Dynamics $(MD)^{12}$ simulations. Recently, Zacharias *et al.*³⁶ found that a single supercell configuration can replace the stochastic sampling of lattice vibrations. Inspired by them, Gunst *et al.* presented a "special thermal displacement" (STD) method, which includes electron–phonon interaction by evaluating the first-principles Landauer–Büttiker transmission of configuration under a single displacement of the atoms.¹³ Thus, the computational burden drastically reduces to the level of non-interacting calculations.

II. METHODOLOGY

In this work, we investigate the electronic transport due to electron-phonon interactions using two first-principles-based approaches (LOA and STD). We apply these methods to two thinfilm nanodevices: 2D silicon junctionless FET and n-i-n FET in order to validate the theoretical conclusion.

The steady-state electronic current from lead $\alpha = L, R$ to the central region including interactions in the device region can be represented as follows:^{20,27,38,39}

$$I_{\alpha} = -2e\langle \dot{N}_{\alpha} \rangle = \frac{2e}{h} \int_{-\infty}^{\infty} d\varepsilon \ T_{\alpha}(\varepsilon),$$

$$T_{\alpha}(\varepsilon) = Tr\{\Sigma_{\alpha}^{<}(\varepsilon)G^{>}(\varepsilon) - \Sigma_{\alpha}^{>}(\varepsilon)G^{<}(\varepsilon)\},$$
(1)

where $\dot{N}_{\alpha} = \sum_{k} c_{k\alpha}^{\dagger} c_{k\alpha}$ is an operator for electronic particle number, $T_{\alpha}(\varepsilon)$ is the transmission function, $\Sigma_{\alpha}^{\leq}(\varepsilon)$ is the lesser/greater self-energy of lead α , and $G^{\leq}(\varepsilon)$ is the lesser/greater Green's function, incorporating all relevant interactions of the device region. The lesser/greater Green's function can be calculated from retarded Green's function (*G*) and advanced Green's function (G^{\dagger}) using the steady-state Keldysh equation, 2^{7-30}

$$G^{\lessgtr} = G\left[\left(\sum_{\alpha} \Sigma_{\alpha}^{\lessgtr}\right) + \Sigma_{\text{int}}^{\lessgtr}\right] G^{\dagger}, \qquad (2)$$

where Σ_{int} and $\Sigma_{int}^{\$}$ are the retarded and lesser/greater interaction self-energy, respectively, accounting for any interaction in the device region. In this paper, we will consider only electron–phonon interaction. Hence, interaction self-energy Σ_{int} can be represented as electron–phonon interaction self-energy Σ_{ph} . The retarded Green's function *G* including electron–phonon interaction is given by the Dyson equation,^{27–30}

$$G = g_0 + g_0 \Sigma_{ph}[G] G, \qquad (3)$$

where g_0 is the non-interacting retarded Green's function.

If no interactions in the device region are considered, we get $\Sigma_{int} = \Sigma_{int}^{\leq} = 0$ and $G = g_0$, which turn Eq. (2) into

$$G^{\lessgtr} = g_0^{\lessgtr} = g_0 \bigg[\sum_{\alpha} \Sigma_{\alpha}^{\lessgtr} \bigg] g_0^{\dagger}, \tag{4}$$

where $g_0^{\$}$ is the non-interacting lesser/greater Green's function. After some rewriting, Eq. (1) turns into the renowned Landauer–Büttiker formula for the non-interacting current,

$$I_{0} = \frac{2e}{h} \int_{-\infty}^{\infty} d\varepsilon \ T_{0}(\varepsilon) [f_{L}(\varepsilon) - f_{R}(\varepsilon)],$$

$$T_{0}(\varepsilon) = \operatorname{Tr} \Big\{ \Gamma_{L} \ g_{0} \ \Gamma_{R} \ g_{0}^{\dagger} \Big\}.$$
(5)

To calculate current due to electron–phonon interaction, the electron–phonon interaction self-energy Σ_{ph} should be calculated. We can represent interaction self-energies as^{32,38,40}

$$\Sigma_{ph}^{\S}(\varepsilon) = \sum_{\lambda} i \int_{-\infty}^{\infty} \frac{d\omega_{\lambda}}{2\pi} M_{\lambda} \ D_{0}^{\S}(\omega_{\lambda}) \ \lambda \ G^{\S}(\varepsilon - \omega_{\lambda}) M_{\lambda}, \tag{6}$$

$$\Sigma_{ph} = \frac{1}{2} \left(\Sigma_{ph}^{>} - \Sigma_{ph}^{<} \right) - \frac{i}{2} \mathscr{H} \left(\Sigma_{ph}^{>} - \Sigma_{ph}^{<} \right), \tag{7}$$

where M_{λ} represents electron–phonon coupling matrix for phonon mode λ , ω_{λ} are phonon frequencies, D_0^{\leq} is the lesser/greater free phonon Green's functions, and \mathcal{H} is the Hilbert transform.

The solution of the Dyson equation [Eq. (3)] requires an iterative scheme with specific convergence criteria. Green's function at N^{th} iteration can be assumed as $G_N \simeq G_{N-1}$, which leads to the representation of the Dyson equation as

$$G_N = \left[g_0^{-1} - \Sigma_{ph} [G_{N-1}]\right]^{-1}.$$
 (8)

The Dyson equation [Eq. (8)] and Keldysh equation [Eq. (2)] along with Eqs. (6) and (7) can be solved iteratively to calculate the current including electron–phonon interaction. The scheme is called self-consistent Born approximation (SCBA), which is illustrated in Fig. 1.

Let us rewrite the Dyson equation [Eq. (8)] in the Taylor series expansion as

$$G_N = g_0 + g_0 \Sigma [G_{N-1}] g_0 + g_0 \Sigma [G_{N-1}] g_0 \Sigma [G_{N-1}] g_0 + \cdots, \quad (9)$$

where Σ_{ph} is written as Σ for simplicity. From Eq. (9), the SCBA Green's function at first iteration G_1 is represented as

$$G_1 = g_0 + g_0 \Sigma[g_0] g_0 + g_0 \Sigma[g_0] g_0 \Sigma[g_0] g_0 + \cdots .$$
(10)

The SCBA Green's function contains an infinite number of terms where some terms are currently conserving. The G_1 does not necessarily preserve the conservation law since there are higher-order non-conserving terms according to the corresponding scattering order.²² The current conservation law is fulfilled when conserving terms are dominant over non-conserving ones. Therefore, a higher number of iterations may be required to fulfill the conservation law in the case of strong electron–phonon interactions, thereby increasing the computational cost.

If we calculate only the conserving terms in the SCBA Green's function, the current conservation law is satisfied at each scattering order. As only the lowest order conserving terms are considered, the method is called Lowest Order Approximation (LOA). The first-order LOA can be written as

$$G_{1LOA} = g_0 + g_0 \Sigma[g_0] g_0, \tag{11}$$

where the interaction self-energy $\Sigma[g_0]$ is calculated only from the non-interacting Green's function g_0 . Reference 23 showed that 1st order LOA is very similar to the SCBA in the weak-scattering regime. However, we can apply the Langreth theorem^{24,27} to Eq. (12) to obtain the lesser/greater Green's function from the LOA Green's function directly,

$$G_{1LOA}^{\leq} = g_0^{\leq} + g_0 \Sigma[g_0] g_0^{\leq} + g_0 \Sigma^{\leq} [g_0] g_0^{\dagger} + g_0^{\leq} \Sigma^{\dagger} [g_0] g_0^{\dagger}.$$
(12)



However, in literature, some articles use an alternative approach to calculate directly the lesser/greater LOA Green's function, G_{LOA}^{\leq} , from retarded Green's function, G_{LOA} , using Keldysh equation [Eq. (2)] instead of Langreth theorem.^{20,31–35} However, this approach is shown here to be less accurate for the case of lowest order approximation. Keldysh equation can only be used to calculate lesser/greater Green's function from retarded Green's function for



FIG. 2. (a) Junctionless silicon thin film FET with source, channel, and drain doping of 10^{20} cm⁻³ (n-type) and $\langle 100 \rangle$ crystallographic orientation. The gate length $L_G = 7$ nm, the source and drain length $L_{S/D} = 6.5$ nm, and the thickness is 1.4 nm (total length is 20 nm with 740 atoms). (b) Current vs gate to source voltage, V_{GS} , for a drain-source voltage $V_{DS} = 0.05$ V and at 300 K. (c) Current vs drain to source voltage, V_{DS} , for a gate-source voltage $V_{GS} = 0$ V.

the case of self-consistent Born approximation. In the case of lowest order approximation if we use the Keldysh equation, we get

where $g_0^{\leq} = g_0 \left[\sum_{\alpha} \Sigma_{\alpha}^{\leq} \right] g_0^{\dagger}$ [Eq. (4)]. Here, the first four terms of Eq. (14) are similar to that of Eq. (13). The last three terms of

Eq. (13) are redundant and will cause incorrect results. Therefore, the Keldysh equation is not suitable for calculating lesser/greater Green's function for the case of lowest order approximation. To get a more correct LOA current, we need to use G_{1LOA}^{\leq} calculated in Eq. (12) instead of Eq. (13). Here, we finally get the interacting current equation by substituting $G^{\leq}(\varepsilon)$ with G_{1LOA}^{\leq} in Eq. (1),

$$I_{\alpha} = \frac{2e}{h} \int_{-\infty}^{\infty} d\varepsilon \ Tr \Big[\Sigma_{\alpha}^{<}(\varepsilon) \Big\{ g_{0}^{>} + g_{0} \Sigma \big[g_{0} \big] g_{0}^{>} + g_{0} \Sigma^{>} \big[g_{0} \big] g_{0}^{\dagger} \\ + g_{0}^{>} \Sigma^{\dagger} \big[g_{0} \big] g_{0}^{\dagger} \Big\} - \Sigma_{\alpha}^{>}(\varepsilon) \Big\{ g_{0}^{<} + g_{0} \Sigma \big[g_{0} \big] g_{0}^{<} \\ + g_{0} \Sigma^{<} \big[g_{0} \big] g_{0}^{\dagger} + g_{0}^{<} \Sigma^{\dagger} \big[g_{0} \big] g_{0}^{\dagger} \Big\} \Big].$$
(14)

The first-principles calculation of LOA current can be computationally burdensome for large devices. The main computational burden comes from the first-principles calculation of the electron–phonon coupling matrix M_{λ} and its integration over a large number of phonon modes λ (e.g., 3000 phonon modes for a device of 1000 atoms). The coupling matrix is calculated from the dynamical matrix and Hamiltonian derivates. The computational burden of Dynamical matrix calculation can be reduced significantly by using classical force-field calculation using Tersoff potential.⁴¹ In repeated two-probe devices, where the atomic configuration of the central region is a repetition of the electrode unit cell along the transport direction, Hamiltonian derivates of the central region



FIG. 3. Projected local device density of states (DDOS) of junctionless FET shows little change between non-interacting and interacting devices in the ON state, but the OFF state shows a decrease in the conduction band edge (from 0.9 to 0.6 eV) in the channel region due to electron–phonon interaction. ε_L and ε_R denote the left and right electrode Fermi levels.

can be approximated from that of the electrode unit cell. Additionally, instead of using a large number of phonon modes, we can sum the phonon modes in energy intervals to create new effective phonon modes. The sufficient number of effective phonon modes is usually less than 50, which further reduces the computation cost.

An alternative way to include electron–phonon interaction on current is the stochastic sampling of lattice vibrations. However, when stochastic sampling with these methods is performed, many transmission calculations must be done with increasing computational cost. Recently, Zacharias *et al.*³⁶ discovered that a single supercell configuration is representative of all the samples of lattice vibrations. Calculating the non-interacting Landauer–Büttiker transmission T_0 of that single lattice configuration is enough to obtain interacting current. The method is known as Special Thermal Displacement (STD) method.¹³ The interacting current due to electron–phonon interaction can be calculated as the thermally averaged current for atomic displacements, $u_\lambda(T, V)$, given by¹³

$$I(V,T) = \frac{2e}{h} \int dE \langle \mathcal{T}(E,T) \rangle [f_L - f_R],$$

$$\langle \mathcal{T}(E,T) \rangle = \Pi_\lambda \int du_\lambda \frac{\exp(-u_\lambda^2/2\sigma_\lambda^2)}{\sqrt{2\pi\sigma_\lambda}} \langle \mathcal{T}(E,\{u_\lambda\}) \rangle.$$
(15)

According to Ref. 13, sampling of displacements, $u_{\lambda}(T, V)$, can be replaced by a single displacement given by

$$u_{STD}(T) = \sum_{\lambda} s_{\lambda} (-1)^{\lambda - 1} \sigma_{\lambda}(T) \mathbf{e}_{\lambda}.$$
 (16)

If we compare the Tailor expansion of Eq. (15) around u_{λ} with Taylor expansion around u_{STD} , both expressions tend to be the same for large systems. Because in large systems, for each pair of degenerate phonon modes, the corresponding terms of the Taylor series cancel each other, resulting in only the even-order terms, which are similar for both Taylor series. Thus, the STD configuration approximates the correct thermally averaged current.

III. RESULTS

The first device we investigate for electron–phonon interaction is a 2D silicon junctionless double-gated FET with a 7 nm gate length [Fig. 2(a)]. The electronic transport occurs along the $\langle 100 \rangle$ crystallographic direction (horizontal direction) under the quantum confinements in the vertical direction. The surfaces of 2D silicon structure in a confined direction are passivated with Hydrogen atoms, and the out-of-plane direction is periodic. Two 1 nm-thick SiO₂ dielectric layers surround the silicon structure in confined directions. The device has the same n-type doping in the source, channel, and drain region. That is why it is called junctionless FET due to no junction in the transport direction.

In the first principles calculation (DFT), we use Local Density Approximation (LDA) in combination with norm-conserving pseudopotentials. We use a single-zeta-polarized basis set for Si and H atoms along with 9 k-points in the periodic direction and 87 k-points in the transport direction. In order to calculate electron-phonon interaction either by LOA method or by STD method, we need In the current vs gate voltage characteristics graph [Fig. 2(b)], the comparison between non-interacting currents and interacting currents (calculated by LOA and STD method) is shown. We observe that electron-phonon interaction has more and opposite effect in the OFF-current than ON-current. This phenomenon can be explained by the scattering and tunneling effect. In the ON-current state, electron-phonon scattering is dominant but not too strong due to the very short channel length. That is why, ON-current is reduced due to electron-phonon interaction, but the reduction is not too



FIG. 4. (a) n-i-n silicon thin film FET with source and drain doping of 10^{21} cm⁻³ and (100) crystallographic orientation. The gate length $L_G = 7$ nm, the source and drain length $L_{SID} = 6.5$ nm, and the thickness is 1.4 nm (total length is 20 nm with 740 atoms). (b) Current vs gate to source voltage, V_{GS} , for a source–drain voltage $V_{SD} = 0.1$ V and at 300 K. (c) Current vs drain to source voltage, V_{DS} , for a gate-source voltage $V_{GS} = 0$ V.



FIG. 5. Projected local device density of states (DDOS) of n-i-n FET shows little change between non-interacting and interacting devices in the ON state, but the OFF state shows a decrease in the conduction band edge (from 0.7 to 0.6 eV) in the channel region due to electron–phonon interaction. ε_L and ε_R denote the left and right electrode Fermi levels.

high. In the OFF-current state, the tunneling effect is much greater than the scattering effect. This leads to an increase in OFF-current up to four orders of magnitude and degradation of subthreshold slope from 100 to 150 mV/dec.

Figure 2(c) shows the current vs drain voltage characteristics for non-interacting and interacting cases. The ON-current reduction is due to the electron-phonon scattering effect. In Figs. 2(b) and 2(c), the interacting currents obtained by LOA and STD methods show excellent agreement. Since the two methods are very different, it can be perceived that these two approaches for interacting with current calculation are valid.

The projected local device density of states of the junctionless FET in Fig. 3 shows little change in the ON current stage due to electron-phonon interaction, which supports the low ON-current reduction. In the OFF-current stage, a considerable decrease in the conduction band edge (from 0.9 to 0.6 eV) in the channel region is shown. The decrease of the conduction band edge indicates the large increase of OFF-current due to electron-phonon interaction.

The second device under investigation is similar to the first device except for the doping profile. It is a two-dimensional silicon n-i-n double-gated FET with a 7 nm gate length [Fig. 4(a)]. Unlike junctionless FET, this device has an intrinsic silicon region under the gates and an n-doping region outside the gate. The n-type doping of 10^{21} cm⁻³ is ten times of the junctionless case. The electronic transport in this device occurs along the $\langle 100 \rangle$ crystallographic

direction (horizontal direction) under the quantum confinements in the vertical direction similar to the junctionless FET. The hydrogen passivation and dielectric layers of this device are the same as the first device. The first-principles calculation and classical force field calculation of the n-i-n FET are the same as the junctionless FET.

Figure 4(b) shows the interacting and non-interacting current vs gate voltage of the n-i-n FET. The interacting current is calculated by LOA and STD methods. The ON-current reduction and OFF-current increase due to electron-phonon interaction follow the trend of the junctionless device. The explanation for the change in current is same as for the first device. However, n-i-n FET has a lower OFF-current increase than the junctionless device. The OFF-current is increased up to ten times, and the subthreshold slope is degraded from 150 to 185 mV/dec due to electron-phonon interaction.

TABLE I. Comparison among different parameter	50	or the	two	aevices
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Devices	Increase in OFF-current	Degradation in subthreshold slope (mV/dec)	Decrease in conduction bandedge (eV)
Junctionless FET	$\frac{10^4}{10^1}$	100 to 150	0.9 to 0.6
n-i-n FET		150 to 185	0.7 to 0.6

	Proposed LOA method (for a device consisting of 740 atoms)	Conventional LOA (for a device consisting of only 56 atoms)
Time spent on IV curve (in terms of core hours)	90 h	468 h

 TABLE II. Comparison between proposed and conventional method⁴² in terms of computational time.

Figure 4(c) shows the current vs drain voltage characteristics for non-interacting and interacting cases. The ON-current is reduced due to the electron-phonon interaction. Again, in this n-i-in FET, the interacting currents calculated by LOA and STD methods show excellent agreement, which reconfirms that the two methods for interacting current calculation are valid.

The projected local device density of states of the n-i-in FET in Fig. 5 shows little change in the ON current stage due to electron-phonon interaction, which supports the low ON-current reduction. In the OFF-current stage, a decrease in the conduction band edge (from 0.7 to 0.6 eV) in the channel region is shown. The decrease of the conduction band edge indicates the increase of OFF-current due to electron-phonon interaction.

Table I presents the comparison among the various parameters of junctionless FET and n-i-in FET. The junctionless device seems to have a greater change in parameters due to electron-phonon interaction.

Table II presents the comparison between our proposed LOA method and a conventional LOA method⁴² in terms of computational time required for the IV curve. The conventional LOA method requires higher time for simulating a device of only 56 atoms, whereas our device consists of 740 atoms and still requires lesser time using our proposed method.

IV. CONCLUSION

In summary, we have made several approximations on the LOA method, such as classical force-field calculation of dynamical matrix, a small number of effective phonon modes, and coupling matrix calculation of repeatable electrode unit cell. These pave the way for calculating electron-phonon interaction with modest computational resources. Among the two existing approaches of the direct LOA method, the Langreth theorem-based approach is theoretically shown to be accurate. To validate the approach, we have compared the output of the LOA method with that of the STD method. The STD method does not require the burdensome calculation of electron-phonon coupling matrix and thereby reduces the computational cost greatly. We have demonstrated phononlimited electronic transport for junctionless FET and n-i-in FET using the two first-principles methods. As both devices maintain low electron-phonon scattering, 1st LOA is shown to be sufficient to give accurate results. Both methods show a similar decrease in ON-current, increase in OFF-current, and degradation of subthreshold slope in the two devices. Since the two very different methods provide similar outputs for both devices, it confirms that the two

methods for interacting current calculation are justified. The computational strategy of the STD method makes it appealing for device modeling where the LOA method may not work due to strong electron–phonon scattering.

AUTHOR DECLARATIONS

Conflict of Interest

The authors have no conflicts to disclose.

Author Contributions

Md. Samzid Bin Hafiz: Conceptualization (lead); Data curation (lead); Formal analysis (lead); Investigation (lead); Methodology (lead); Software (lead); Validation (lead); Visualization (lead). Quazi Deen Mohd Khosru: Supervision (lead); Writing – original draft (supporting). Momotaz Begum: Formal analysis (supporting); Investigation (supporting). Bimal Chandra Das: Formal analysis (supporting); Investigation (supporting).

DATA AVAILABILITY

The data that support the findings of this study are available from the corresponding author upon reasonable request.

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